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Marvell to Demonstrate Industry-Leading Flash-based Innovations at Flash Memory Summit

Marvell booth features live demos of groundbreaking Flash solutions for home and enterprise cloud computing

SANTA CLARA, Calif., Aug. 21, 2012 /PRNewswire/ -- Marvell (Nasdaq: MRVL), a worldwide leader in integrated silicon solutions, is demonstrating its industry leadership in Flash-based solutions at the Flash Memory Summit being held at the Santa Clara Convention Center from August 21 to 23. Located in booth #800-802, Marvell will feature a broad spectrum of live demonstrations ranging from turnkey chipsets to board-level adapters that utilize NAND flash memory.

(Logo: https://photos.prnewswire.com/prnh/20100719/SF36559LOGO-b)

"The combined impact of big data, mobile computing and data-hungry apps is driving consumers and enterprises to embrace Flash memory," said Alan Armstrong, vice president of marketing for the Storage Business Group at Marvell Semiconductor, Inc. "Marvell is a leading technology provider of silicon, silicon/software and board-level Flash-driven solutions that directly address end-to-end cloud computing from the home cloud to enterprise and public clouds. As a leader in ARM-based storage, networking and application processing, Marvell has the unique ability to create innovative reference design and turn-key solutions. Our breadth of offerings gives our OEM customers the flexibility and range of choice to select the right solution to meet their cost, power, density and performance needs."

FEATURED TECHNOLOGIES:

Marvell® DragonFly™ General Availability: Marvell will announce the general availability of the DragonFly platform at the Flash Memory Summit. The breakthrough DragonFly platform combines Marvell's SoC technology with its newly designed circuit boards, and plugs directly into commercially available servers with a PCIe slot. The result is a leading-edge systems solution that can achieve 10-100x lower latency and improved server I/O performance, while reducing power, space and storage capital costs in the datacenter. DragonFly will be commercially available on September 15, 2012.

DragonFly is sold as a PCIe Gen2 x8 adapter with up to 8GB SODIMM ECC DRAM and up to 1.5 TB of external SSD storage; read, write-back and write-thru caching; synchronous low-latency peer-to-peer write-back mirroring; 3.2GB/s of throughput; less than 22us latency; 220K IOPS read; 220K IOPS write; integrated ultracapacitors to protect data in the event of power loss; and a wide variety of host operating system support, including RHEL, KVM, Xen, VMWare and Windows.

Marvell ARM SSD Server Design: Marvell is deeply committed to Flash and to helping OEMs focus their efforts on developing differentiated products that leverage Flash-based solutions. Marvell is distancing itself from the competition as the only one-stop shop for creating an end-to-end ARM SSD server reference design by providing end-to-end silicon solutions including networking, application processors, storage connectivity and NAND flash controllers.

Servers based on Marvell's quad core ARM-based ARMADA® XP SoC products – the world's first quad-core ARM processor designed for enterprise-class cloud computing applications – drive higher CPU utilization and offer demand based scaling. The result is lower power, higher efficiency server solutions for data centers and enterprises that lead to significant savings.

Marvell will also host live demos of its ARM SSD server reference design at the Marvell booth. The demonstration will combine an ARM server reference design based on ARMADA XP and the 88SE9445 PCIe-SAS/SATA controller with SanDisk X100 6Gb/s SATA SSDs. This system reference architecture combines market-leading performance with low power consumption and space efficiency, delivering many game changing benefits for the next-generation of hyperscale cloud computing and enterprise data centers.

SSD-enabling Storage SoCs: Marvell will also offer multiple demonstrations of its storage SoCs used in PCIe SSDs, SAS SSDs, and Thunderbolt-based SSDs. These include a high-definition video streaming off a Marvell-powered Thunderbolt-based SSD, PCIe SSD reference designs that show powerful I/O acceleration for both bare-metal and virtual server applications, and several finished product examples of OEM customers that leverage Marvell's storage SoC's for HBAs and SSDs.

Examples of bridge-based PCIe SSDs include the use of the Marvell 88SE9485, aPCIe 2.0 x8 to 8x 6Gb/s SAS/SATA I/O controller, and the Marvell 88SE9230, a PCIe 2.0 x2 to 4x 6Gb/s SATA I/O controller. Native PCIe SSD reference designs leverage the Marvell reference board that combine a low-latency non-blocking PLX PCIe switch with multiple Marvell 88NV9145 PCIe-NAND modules. SAS SSDs utilize the Marvell 88SF9210 (6Gb/s dual-port SAS to dual SATA ports protocol converter) and 88SF9110 (6Gb/s dual-port SAS to single SATA port protocol converter). Finally, Thunderbolt-powered SSDs leverage a combination of an Intel Thunderbolt controller with a Marvell PCIe-SATA I/O controller, such as the 88SE9230 and 88SE9182.

Shawn Kung, director of product marketing for the Storage Business Group at Marvell, will be participating in the 202-B Panel: Designing Storage Systems in the SSD Era at Flash Memory Summit on Wednesday, August 22, at 9:50 a.m. PT in room 212.

http://www.flashmemorysummit.com/English/Conference/Seminar_Session_Descriptions.html#S202B

About Marvell

Marvell (NASDAQ: MRVL) is a world leader in the development of storage, communications and consumer silicon solutions. Marvell's diverse product portfolio includes switching, transceiver, communications controller, wireless and storage solutions that power the entire communications infrastructure, including enterprise, metro, home and storage networking. As used in this release, the term "Marvell" refers to Marvell Technology Group Ltd. and its subsidiaries. For more information, visit Marvell.com.

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1 GB = 1 billion bytes; 1 MB = 1 million bytes; based on internal testing of sequential read and write speeds; performance may be lower depending upon host device, OS and application.

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